



#### N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

#### **Features**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- ESD Protected Gate, 1KV (HBM)
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

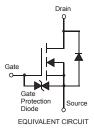
#### **Mechanical Data**

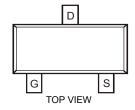
- Case: SOT323
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Solderable per MIL-STD-202, Method 208 (3)
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Terminal Connections: See Diagram
- Weight: 0.006 grams (approximate)

**SOT323** 









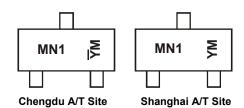
### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN66D0LW-7	SOT323	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

## **Marking Information**



MN1 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)  $\overline{\gamma}_M$  = Date Code Marking for CAT (Chengdu Assembly/ Test site)

Y or  $\overline{Y}$  = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key

Year	2008		2009	2010		2011	2012		2013	2014		2015
Code	V		W	Х		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristi	Symbol	Value	Units	
Drain-Source Voltage		$V_{DSS}$	60	V
Gate-Source Voltage (Note 5)	Continuous	$V_{GSS}$	±20	V
Drain Current (Note 5)	Continuous Continuous @ +100°C Pulsed	l <sub>D</sub>	115 73 800	mA

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation	$P_{D}$	200	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	625	°C/W
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +150	°C

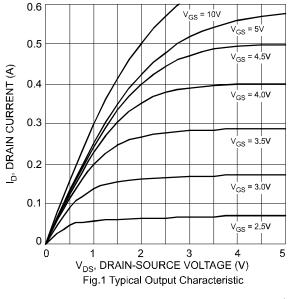
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

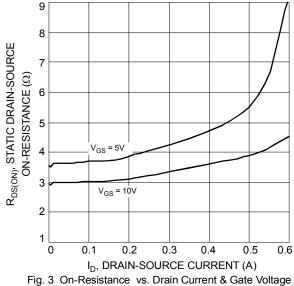
Characteristic			Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	60	70	_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	@ T <sub>C</sub> = +25°C @ T <sub>C</sub> = +125°C	I <sub>DSS</sub>	_	_	1.0 500	μΑ	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
Gate-Body Leakage		I <sub>GSS</sub>	_	_	±5	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage		V <sub>GS(th)</sub>	1.2	_	2.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	@ T <sub>J</sub> = +25°C	RDOVONI	_	3.5	6	Ω	$V_{GS} = 5.0V, I_D = 0.115A$
	@ T <sub>J</sub> = +125°C			3.0	5		V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.115A
Forward Transconductance		<b>9</b> FS	80	_	_	mS	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.115A
DYNAMIC CHARACTERISTICS (Note 7)				•	•	•	
Input Capacitance		C <sub>iss</sub>	_	23	_	pF	
Output Capacitance		Coss	_	3.4	_	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance		C <sub>rss</sub>	_	1.4	_	pF	
SWITCHING CHARACTERISTICS(Note 7)					•		
Turn-On Delay Time		t <sub>D(ON)</sub>	_	10		ns	$V_{DD} = 30V$ , $I_D = 0.115A$ , $R_L = 150\Omega$ ,
Turn-Off Delay Time		t <sub>D(OFF)</sub>	_	33	_	ns	$V_{GEN}$ = 10 $V_{,}$ $R_{GEN}$ = 25 $\Omega$

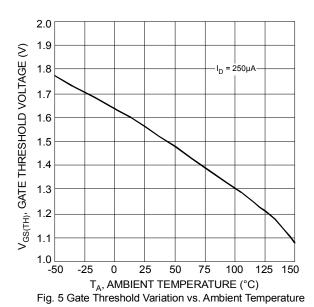
Notes

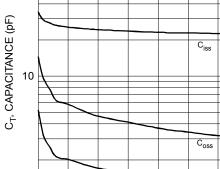
- 5. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Guaranteed by design. Not subject to production testing.

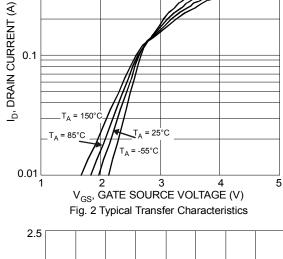












V<sub>DS</sub> = 5V Pulsed

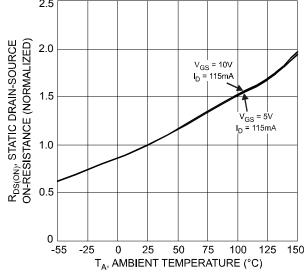


Fig. 4 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

f = 1MHz

100

0



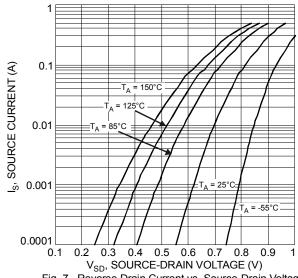
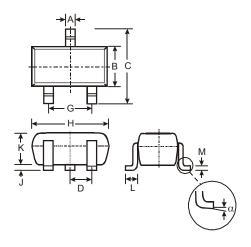


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

## **Package Outline Dimensions**

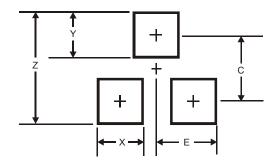
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT323							
Dim	Min	Max	Тур				
Α	0.25	0.40	0.30				
В	1.15	1.35	1.30				
C	2.00	2.20	2.10				
D	-	1	0.65				
G	1.20	1.40	1.30				
Н	1.80	2.20	2.15				
J	0.0	0.10	0.05				
K	0.90	1.00	1.00				
L	0.25	0.40	0.30				
M	0.10	0.18	0.11				
α	0°	8°	-				
All Dimensions in mm							

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.8
Х	0.7
Υ	0.9
С	1.9
E	1.0



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