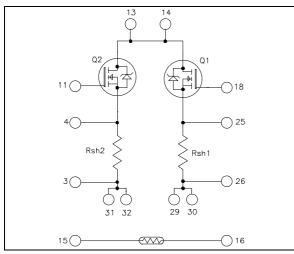
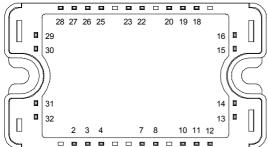


APTML502UM90R020T3AG

Linear MOSFET Power Module





Pins 13/14; 29/30; 31/32 must be shorted together

$$\begin{split} V_{DSS} &= 500 V \\ R_{DSon} &= 90 m \Omega \text{ typ } @ \text{ Tj} = 25^{\circ} C \\ I_D &= 52 A^* @ \text{ Tc} = 25^{\circ} C \end{split}$$

Application

• Electronic load dedicated to power supplies and battery discharge testing

Features

- Linear MOSFET
- Very low stray inductance
- Internal thermistor for temperature monitoring
- High level of integration
- AlN substrate for improved thermal performance

Benefits

- Direct mounting to heatsink (isolated package)
- easy series and parallels combinations for power and voltage improvements
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

Absolute maximum ratings (per leg)

Symbol	Parameter Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		500	V
T	$T_c = 25^{\circ}C$		52*	
I_D	Continuous Drain Current	$T_c = 80$ °C	39*	A
I_{DM}	Pulsed Drain current		200	
V_{GS}	Gate - Source Voltage		±30	V
R_{DSon}	Drain - Source ON Resistance		108	mΩ
P_{D}	Maximum Power Dissipation \bullet $T_c = 25^{\circ}C$		568	W
I_{AR}	Avalanche current (repetitive and non repetitive)		52	A
E_{AR}	Repetitive Avalanche Energy		50	mJ
E_{AS}	Single Pulse Avalanche Energy		3000	1113

- * Output current must be limited to 31A @ T_C=25°C and 22A @ T_C=80°C to not exceed the shunt specification.
- 1 In saturation mode

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



All ratings @ $T_j = 25$ °C unless otherwise specified

Electrical Characteristics (per leg)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 500V ; V_{GS} = 0V$	$T_j = 25$ °C			25	4	
		$V_{DS} = 400V ; V_{GS} = 0V$	$T_j = 125$ °C			250	μΑ	
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 26A$			90	108	mΩ	
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 2.5 \text{mA}$		2		4	V	
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}$				±100	nA	

Dynamic Characteristics (per leg)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		7600		
C_{oss}	Output Capacitance	$V_{\rm DS} = 25V$		1280		pF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		620		

Shunt Electrical Characteristics (per leg)

Symbol	Characteristic		Min	Тур	Max	Unit
R_{sh}	Resistance value			20		mΩ
T_{sh}	Tolerance			2		%
D	Load capacity $ \frac{T_{\text{C}}=25^{\circ}\text{C}}{T_{\text{C}}=80^{\circ}\text{C}} $			20	W	
P_{sh}		$T_{\rm C}=80^{\circ}{\rm C}$			10	VV
I_{sh}	L Ultrent canacity	$T_{\rm C}$ =25°C			31	٨
		$T_{\rm C}=80^{\circ}{\rm C}$			22	Α

Temperature sensor PTC

1 chipci	ature sensor r re					
Symbol	Characteristic		Min	Typ	Max	Unit
R ₂₅	Resistance @ 25°C		1980		2020	Ω
R_{100}/R_{25}	Resistance ratio	Tamb=100°C & 25°C	1.676	1.696	1.716	
R_{-55}/R_{25}	Resistance ratio	Tamb=-55°C & 25°C	0.48	0.49	0.50	
В	Temperature coefficient			7900		ppm/K

Thermal and package characteristics

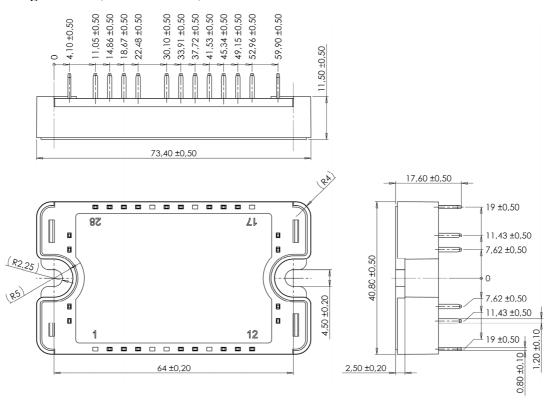
Symbol	Characteristic			Min	Typ	Max	Unit
R_{thJC}	Junction to Case Thermal Resistance MOSFET (per leg)					0.22	°C/W
V_{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz			4000			V
T_{J}	Operating junction temperature range			-40		150	
T _{STG}	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					110	g

2 - 4



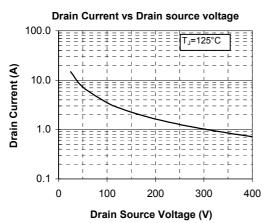
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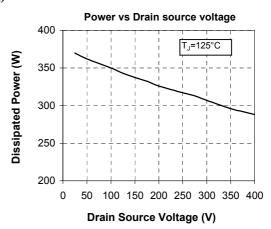
SP3 Package outline (dimensions in mm)



See application note 1901 - Mounting Instructions for SP3 Power Modules on www.microsemi.com

Typical Performance Curve (linear mode) (per leg)







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